

FIG. 1

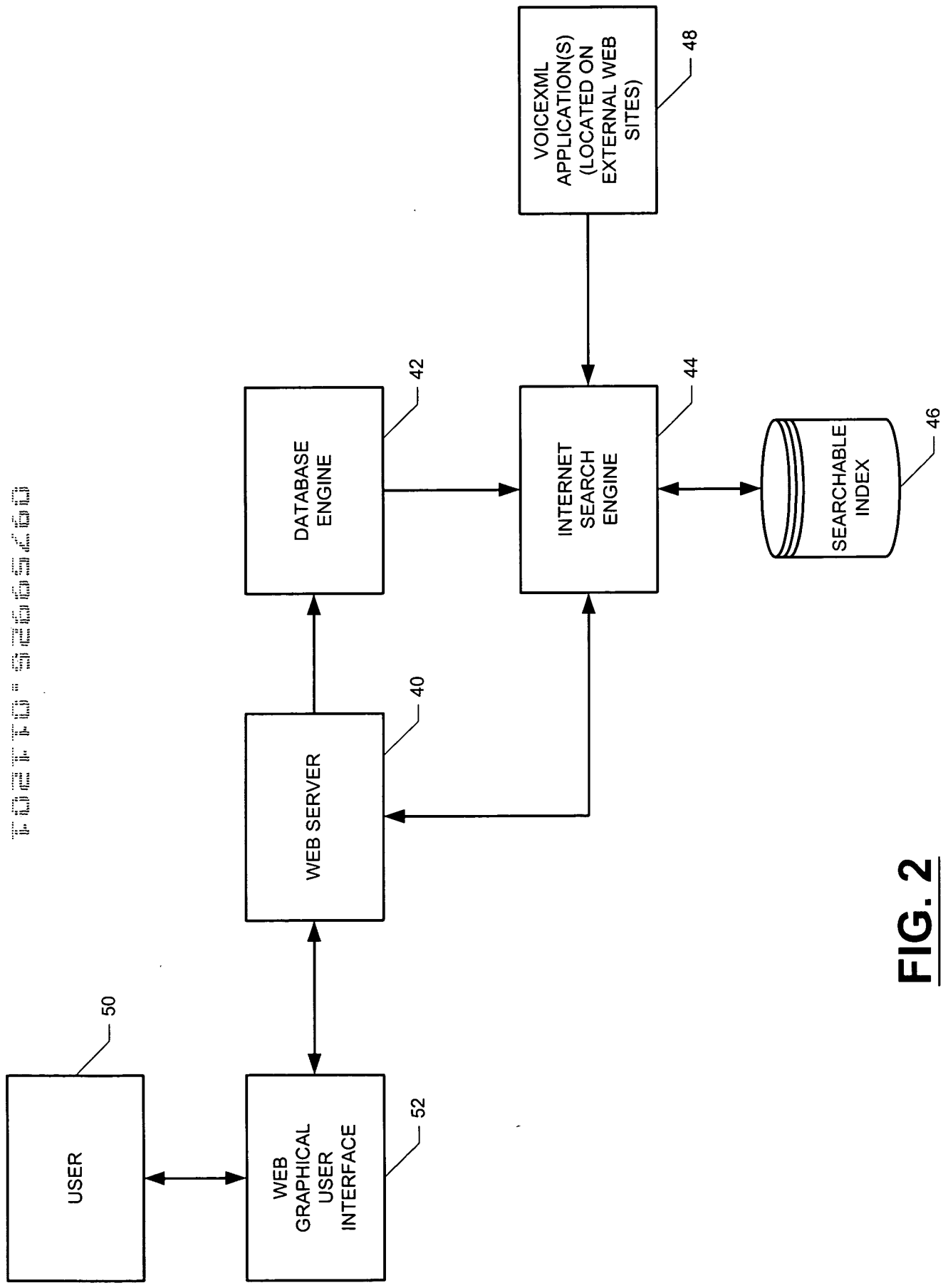


FIG. 2

FIG. 3

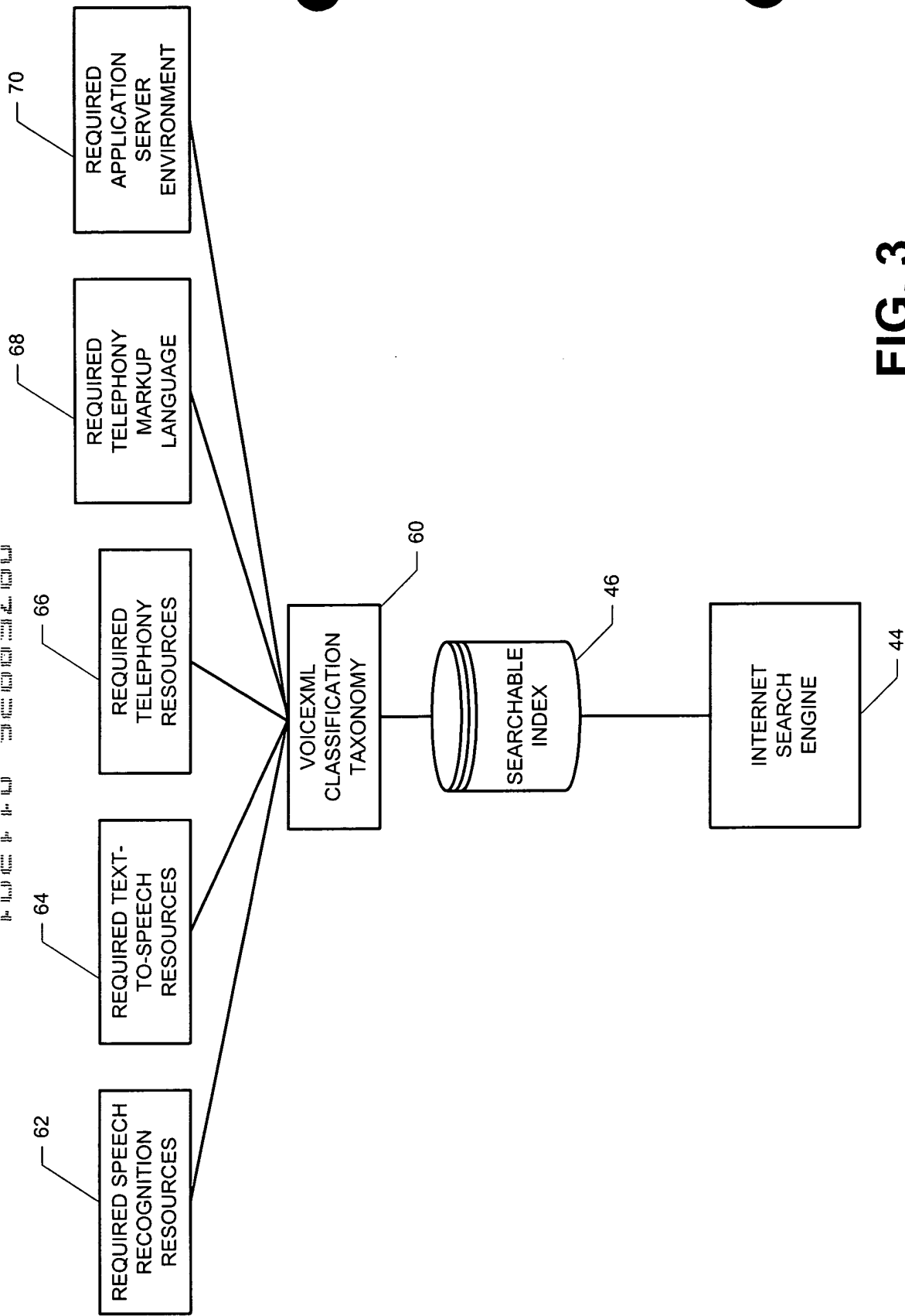


FIG. 3

FIG. 4 is a block diagram of a system architecture.

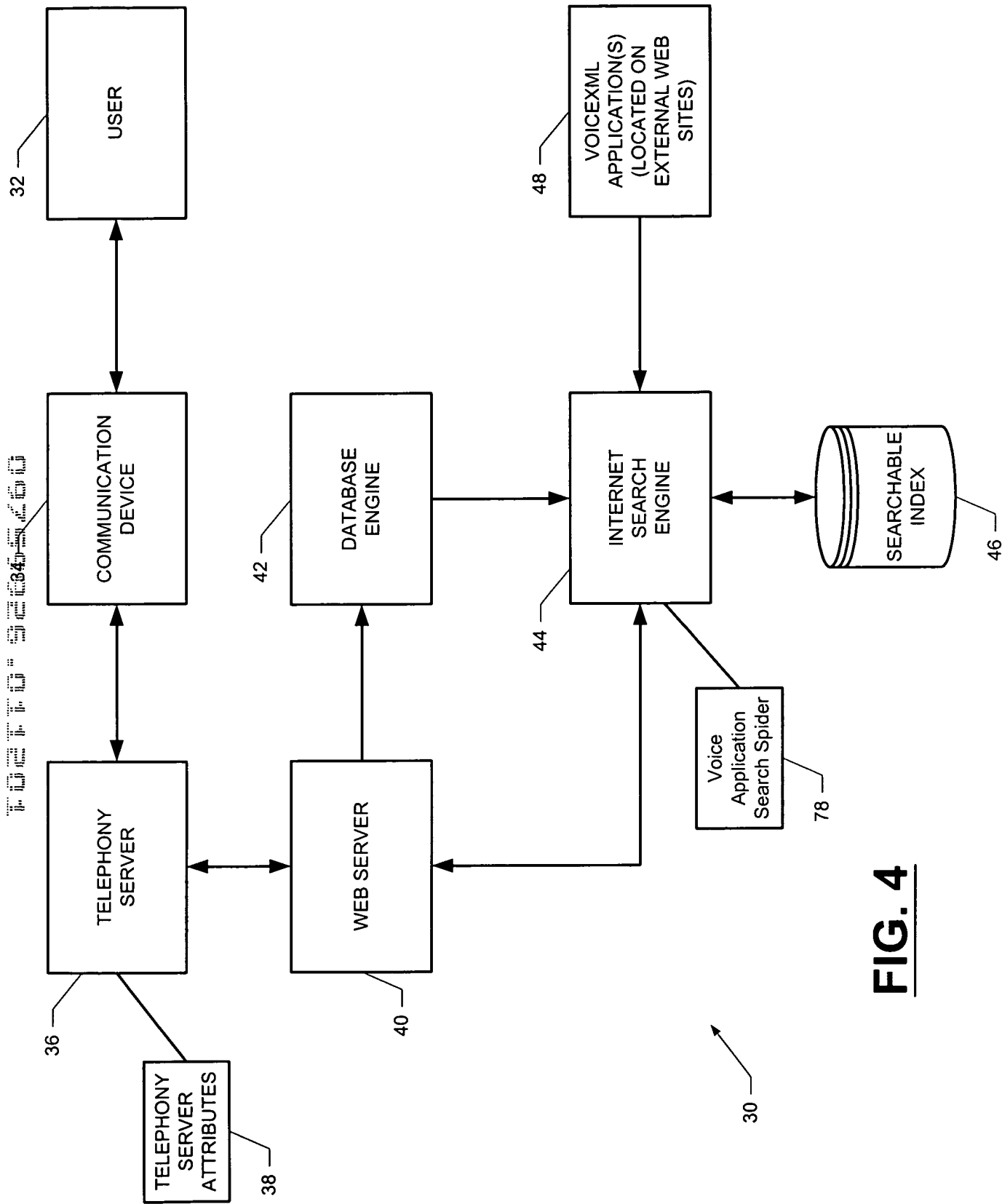
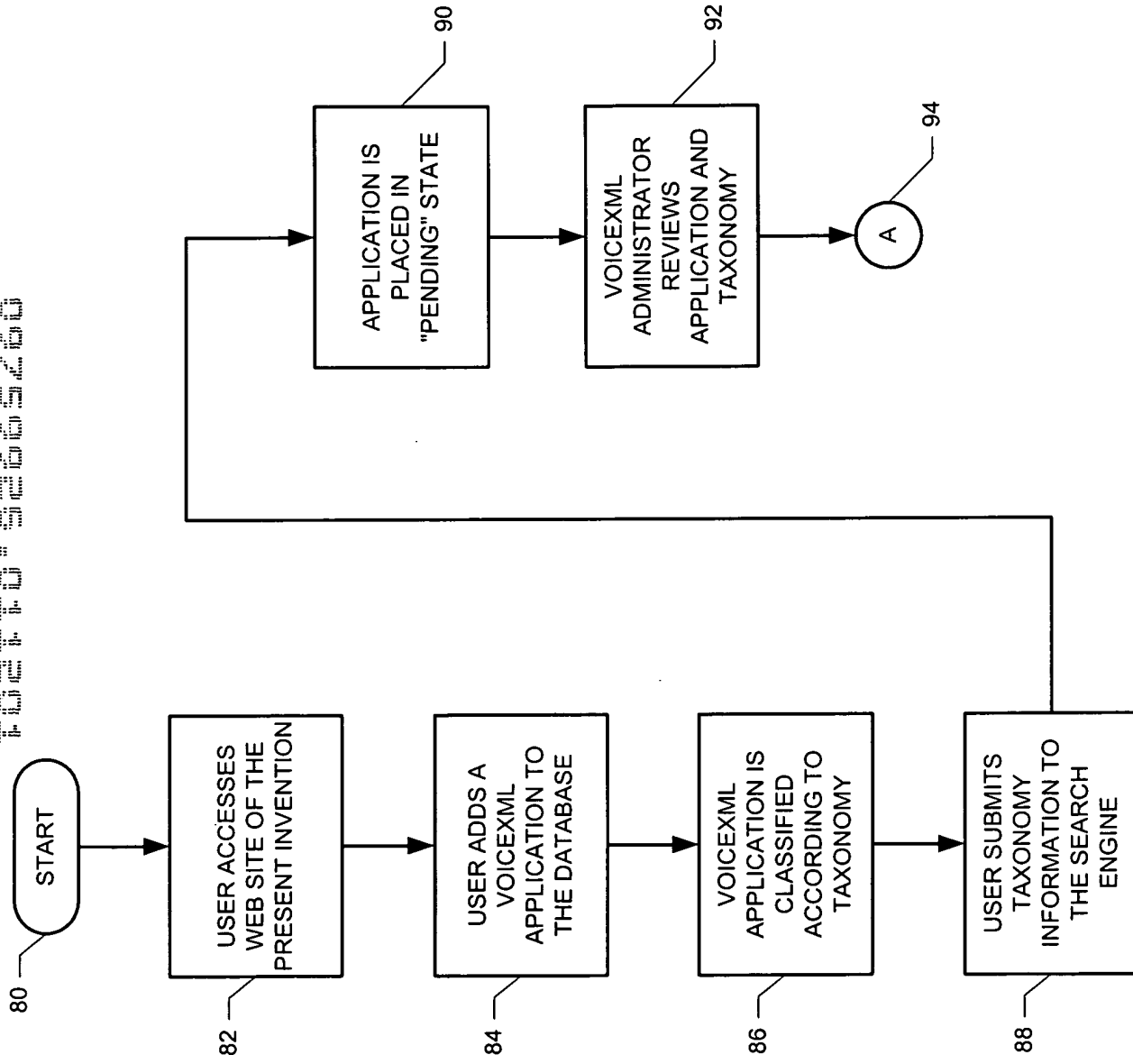


FIG. 4



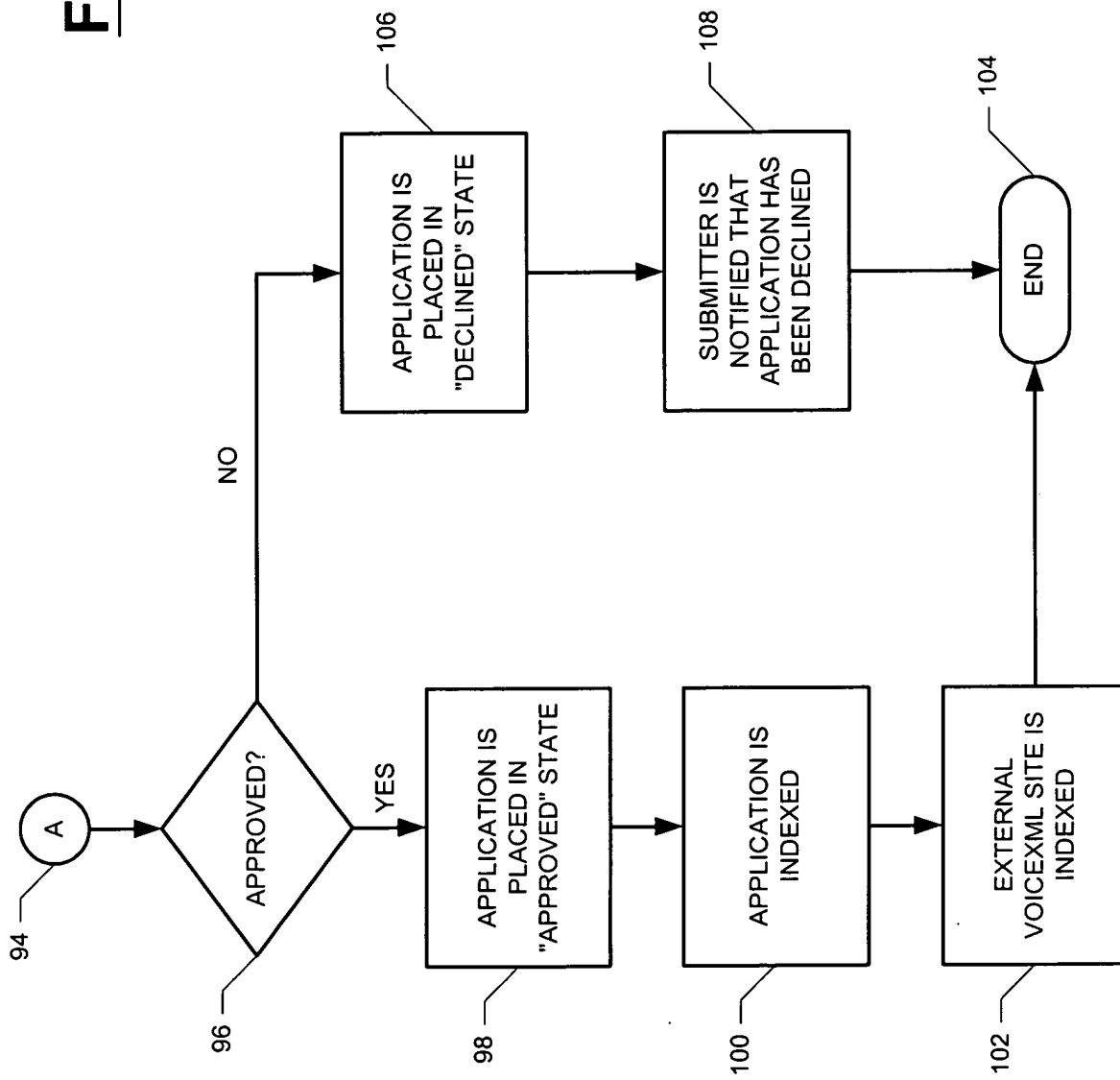
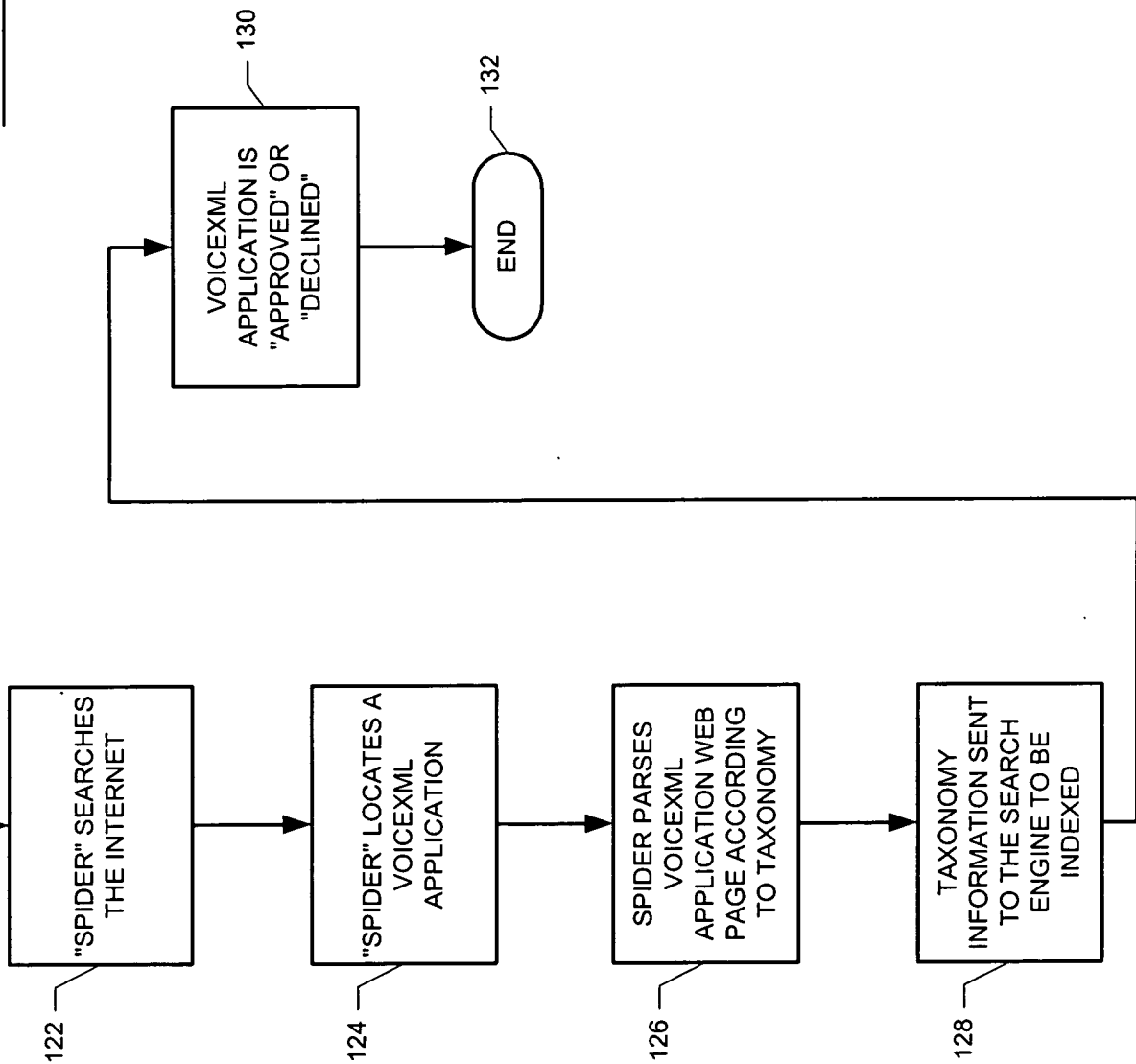


FIG. 6

120 START 5005200

FIG. 7



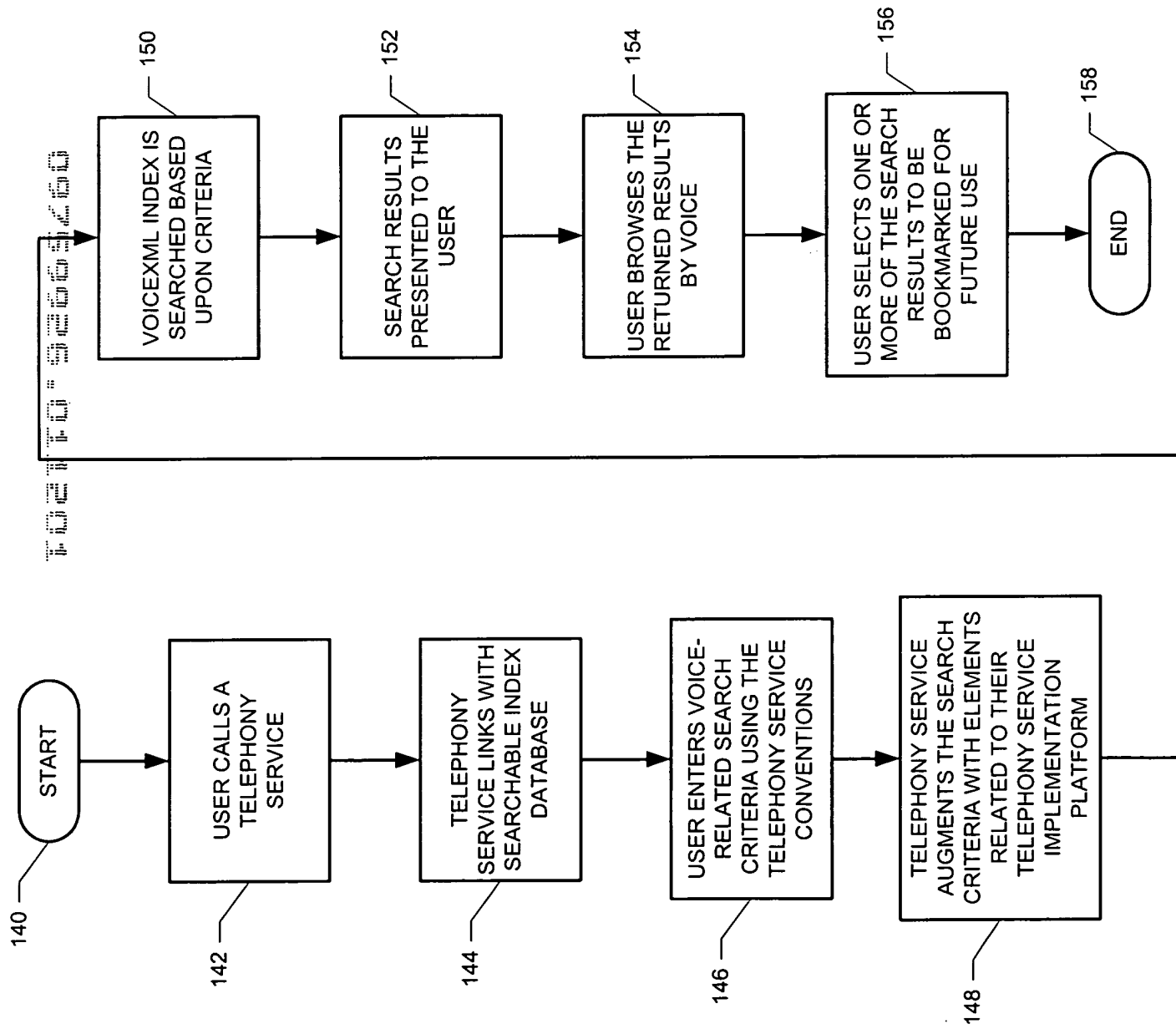


FIG. 8

